

- 📁 Drafts
  - 📄 BRS: 3 and
- 🕒 Pending
- # 📁 Active
  - 📄 L3: (147) (f'6314563' or f'6212607' or f'6212607' or f'5390308' or f'5243699
  - 📄 L4: (5359) memory near? busy
  - 📄 L5: (454) id\$4 near? L4
  - 📄 L6: (0) 5 and 3
  - 📄 L7: (15) 3 and 4
  - 📄 L8: (132) 3 not 7
- 🕒 Failed
- # 📁 Saved
  - 📄 S2: (5351) memory near? busy
  - 📄 S3: (454) id\$4 near? S2
  - 📄 S4: (4) flow near? regulator near? S2
- 📁 Favorites
- 📁 Tagged (6)
- 📁 UDC
- 📁 Queue
- # 📁 Trash

<input type="button" value="Previous"/> <input type="button" value="Next"/> <input type="button" value="Reset"/> <input type="button" value="Clear"/>	<input type="button" value="Go"/>	US-PG-PUB; US-PAT; US-OCR; EPO; JPO; DERIVENT; IBM; TDB	<input checked="" type="checkbox"/> Print
Default operator: <input type="text" value="OR"/>		<input type="checkbox"/> Highlight all hit terms (table)	
<div> <input type="button" value="1"/> <input type="button" value="2"/> <input type="button" value="3"/> <input type="button" value="4"/> <input type="button" value="5"/> <input type="button" value="6"/> <input type="button" value="7"/> <input type="button" value="8"/> <input type="button" value="9"/> <input type="button" value="0"/> <input type="button" value="A"/> <input type="button" value="B"/> <input type="button" value="C"/> <input type="button" value="D"/> <input type="button" value="E"/> <input type="button" value="F"/> <input type="button" value="G"/> <input type="button" value="H"/> <input type="button" value="I"/> <input type="button" value="J"/> <input type="button" value="K"/> <input type="button" value="L"/> <input type="button" value="M"/> <input type="button" value="N"/> <input type="button" value="O"/> <input type="button" value="P"/> <input type="button" value="Q"/> <input type="button" value="R"/> <input type="button" value="S"/> <input type="button" value="T"/> <input type="button" value="U"/> <input type="button" value="V"/> <input type="button" value="W"/> <input type="button" value="X"/> <input type="button" value="Y"/> <input type="button" value="Z"/> <input type="button" value="0-9"/> <input type="button" value="A-Z"/> <input type="button" value="1-0"/> <input type="button" value="1-9"/> <input type="button" value="2-0"/> <input type="button" value="2-9"/> <input type="button" value="3-0"/> <input type="button" value="3-9"/> <input type="button" value="4-0"/> <input type="button" value="4-9"/> <input type="button" value="5-0"/> <input type="button" value="5-9"/> <input type="button" value="6-0"/> <input type="button" value="6-9"/> <input type="button" value="7-0"/> <input type="button" value="7-9"/> <input type="button" value="8-0"/> <input type="button" value="8-9"/> <input type="button" value="9-0"/> <input type="button" value="9-9"/> </div>			

	U	Pub	Document ID	Issue Date	Pages	Title	Current Off	Current XRef	Retrieval Class	
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6778461 B2	20040817	10	Dynamic random access memory device external functionality	365/230.03	365/189.04		Grasso: C
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6745279 B2	20040601		Memory controller	711/105	365/203; 365/204		Morita: Y
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6654861 B2	20031125	14	Method to manage multiple communication queues in an 8-bit	711/156	711/154		Mora: Os
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6530007 B2	20030304	32	Method and apparatus for supporting heterogeneous memory in computer	711/167	711/170; 711/5		Olorig: So
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6463066 B2	20021008		ATM switch	370/412	370/378; 370/395.7		Moriwaki
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6457100 B1	20020924		Scaleable shared-memory multi-processor computer system	711/119	711/122; 711/129		Ignatows
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6438562 B1	20020820		Parallel index maintenance	707/201	707/202		Gupta: Sh
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6424688 B1	20020813	14	Method and apparatus for providing	712/34	345/503		Rhoden: P



- ✎ Drafts
- ⊖ Pending
- 📁 Active
- 📁 Failed
- ⊖ Saved
  - 🔍 S2: {5351} memory near9 busy
  - 🔍 S3: {454} id134 near9 S2
  - 🔍 S4: {4} flow near9 regulator near9 S2
- 📁 Favorites
- 📁 Tagged (5)
- 🔍 UDC
- 📁 Queue
- ⊖ Trash

122 95 100 105 110 115 120 125 130 135 140 145 150 155 160 165 170 175 180 185 190 195 200 205 210 215 220 225 230 235 240 245 250 255 260 265 270 275 280 285 290 295 300 305 310 315 320 325 330 335 340 345 350 355 360 365 370 375 380 385 390 395 400 405 410 415 420 425 430 435 440 445 450 455 460 465 470 475 480 485 490 495 500 505 510 515 520 525 530 535 540 545 550 555 560 565 570 575 580 585 590 595 600 605 610 615 620 625 630 635 640 645 650 655 660 665 670 675 680 685 690 695 700 705 710 715 720 725 730 735 740 745 750 755 760 765 770 775 780 785 790 795 800 805 810 815 820 825 830 835 840 845 850 855 860 865 870 875 880 885 890 895 900 905 910 915 920 925 930 935 940 945 950 955 960 965 970 975 980 985 990 995

24. 15-PGP1B, USPAT. USOCR. EPO. JPO. DERIVENT. IBM. TDB.

 Pleco

Default printer: **OP**

✓ f6p8qx as fa lerra k88x

flow near9-regulator near9:1

	U	Pub	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	Inventor
1		<input checked="" type="checkbox"/>	US 20050111353 A1	20050526	30	Memory management system having a linked list processor	370/229			Zievers, Peter
2		<input checked="" type="checkbox"/>	US 20050103464 A1	20050519	30	Contention resolution in a memory management system	711/100			Zievers, Peter
3		<input checked="" type="checkbox"/>	US 20050097259 A1	20050505	30	Memory management system for a data processing system	711/100			Zievers, Peter
4		<input checked="" type="checkbox"/>	US 20050108464 A	20050519	30	Memory management system operating method for multi-node				ZIEVERS, P J



	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	IR
1	<input checked="" type="checkbox"/>	US 5630092 A	19970513	16	System and method for transferring compressed and uncompressed data	711/111	708/203; 710/68		Carreira; Pat